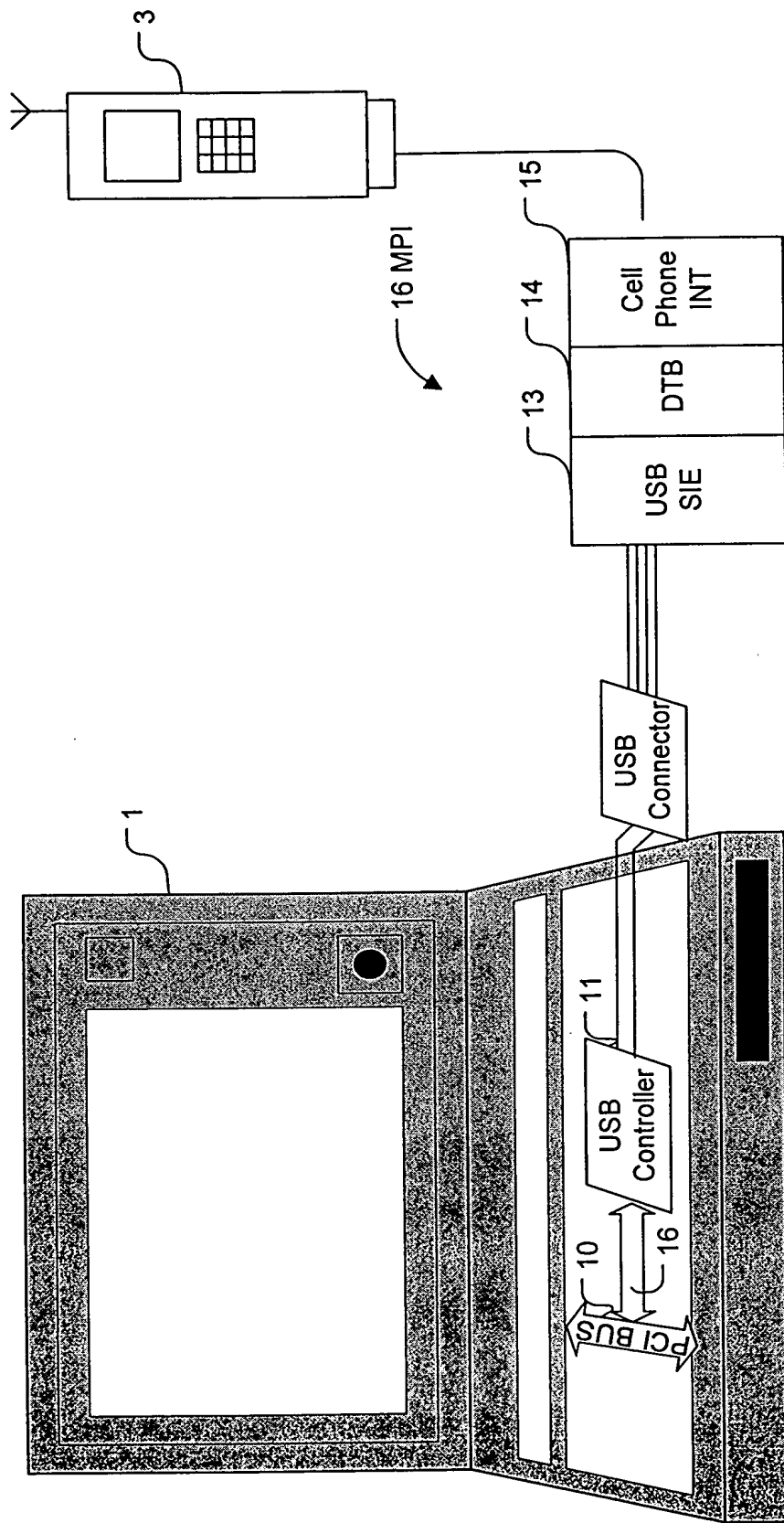
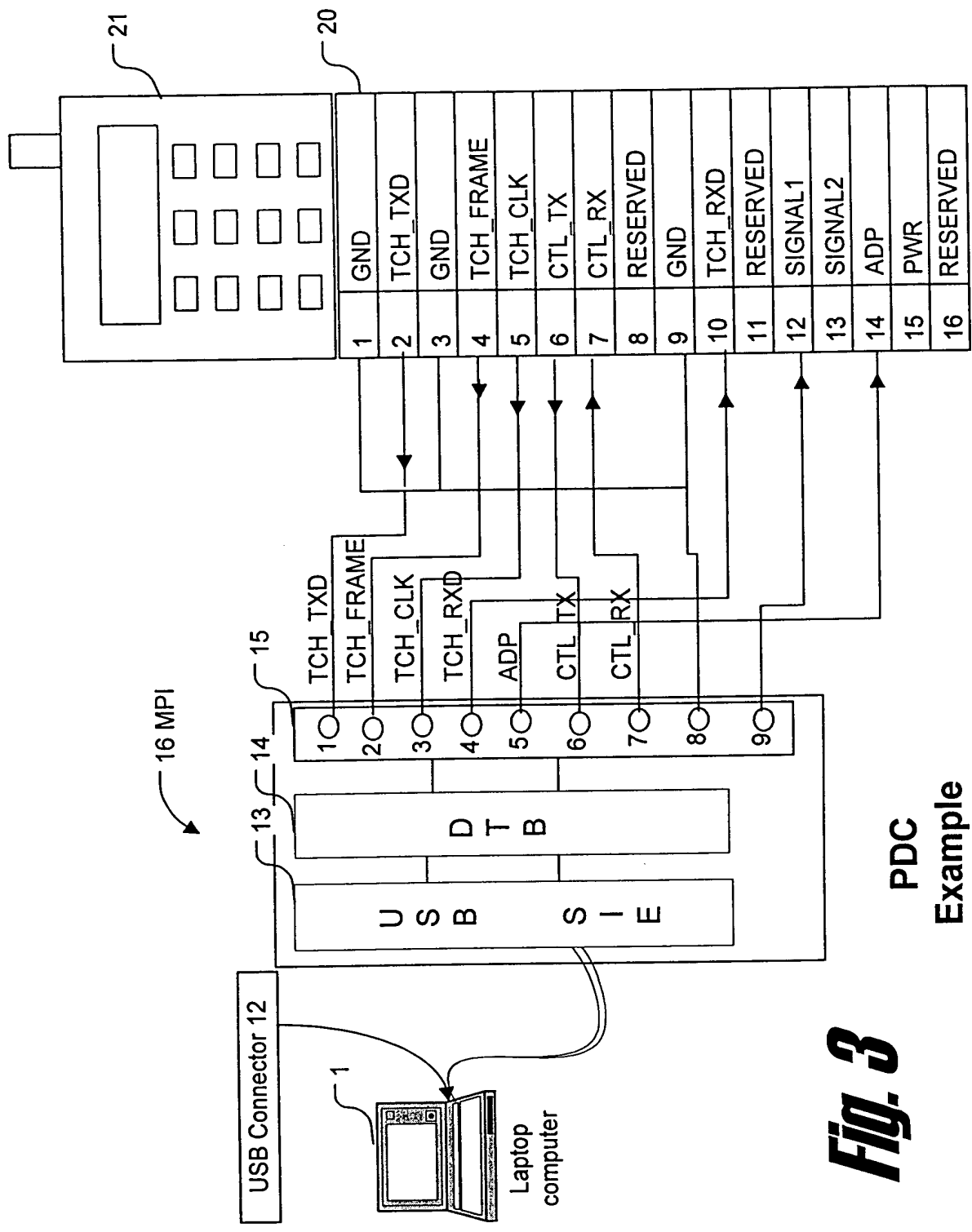


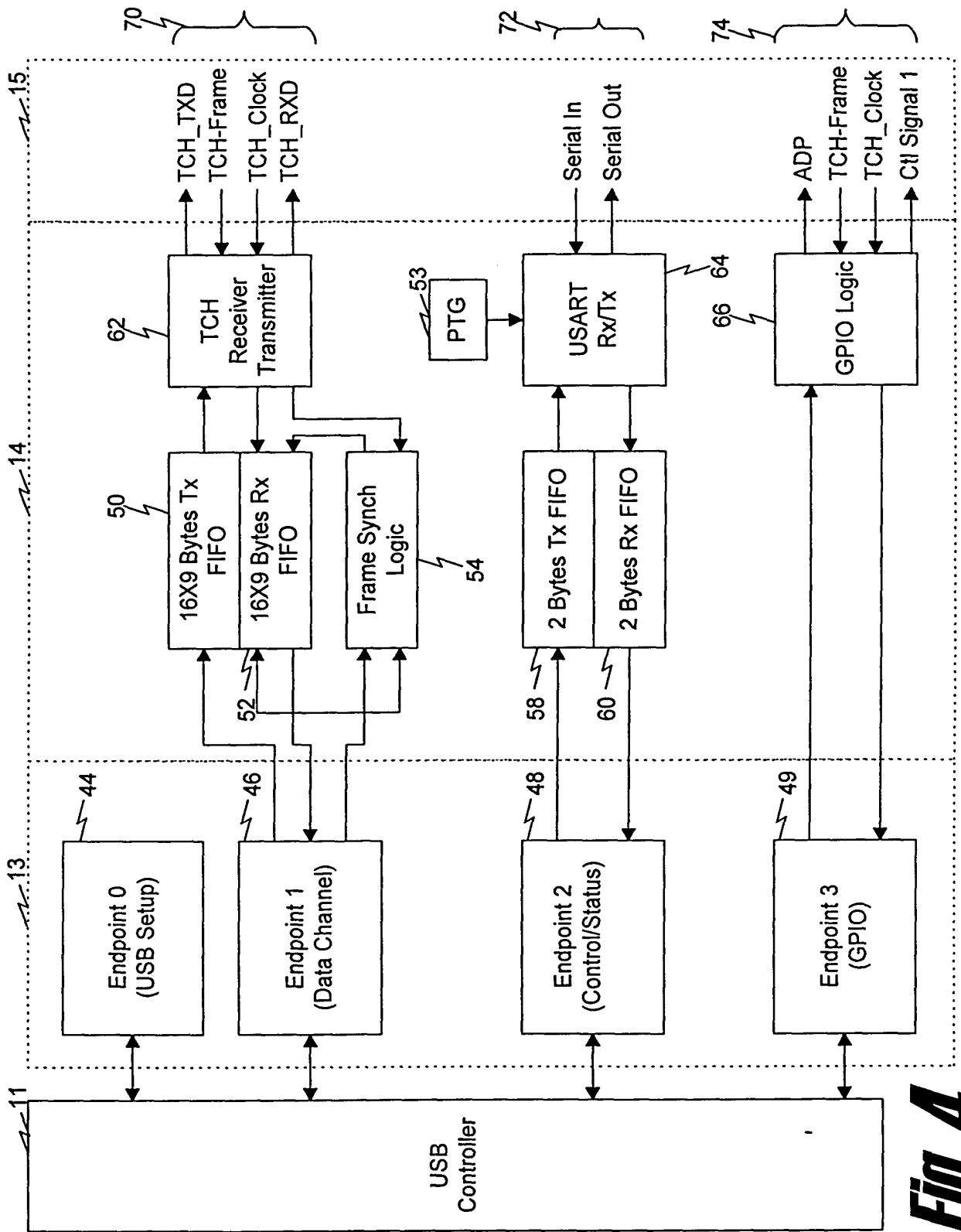
**Fig. 1**



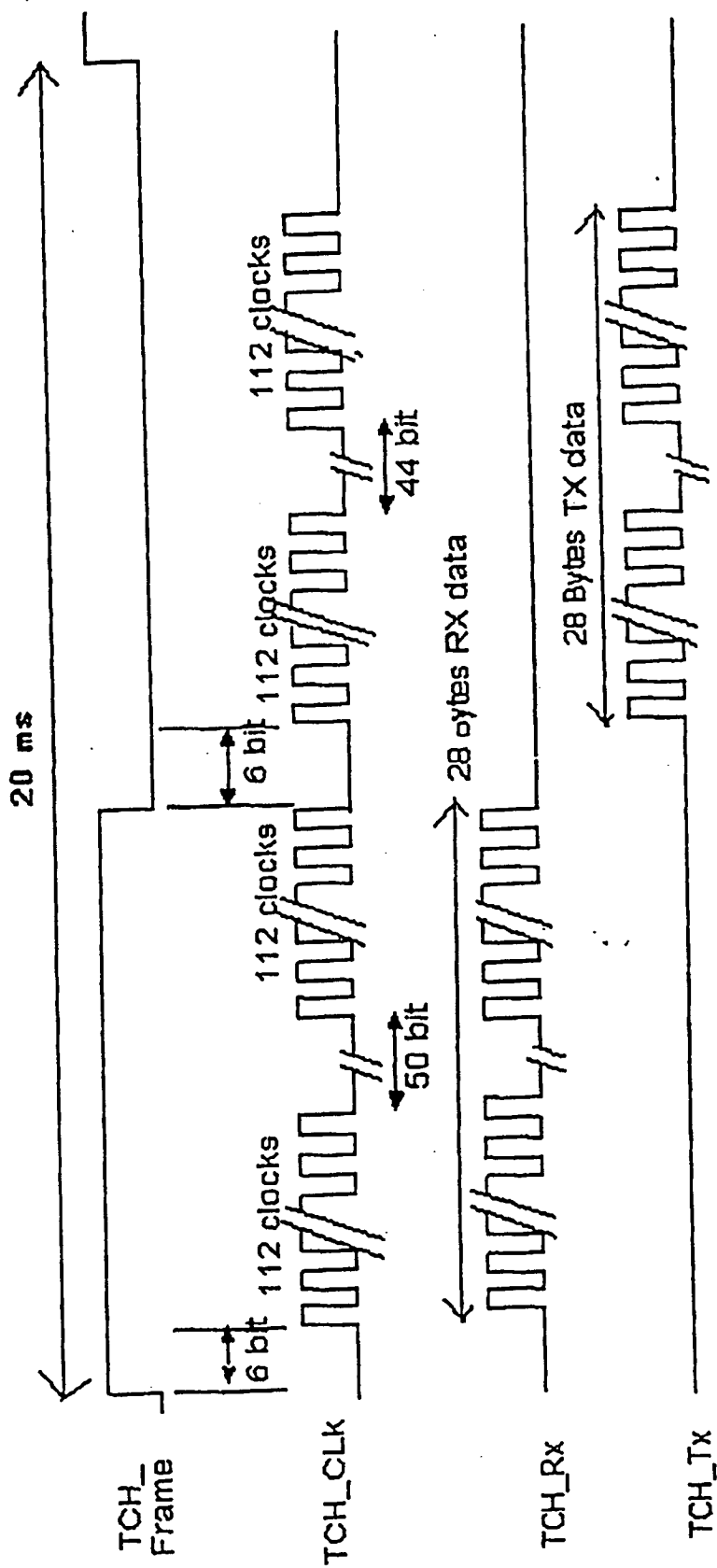
**Fig. 2**



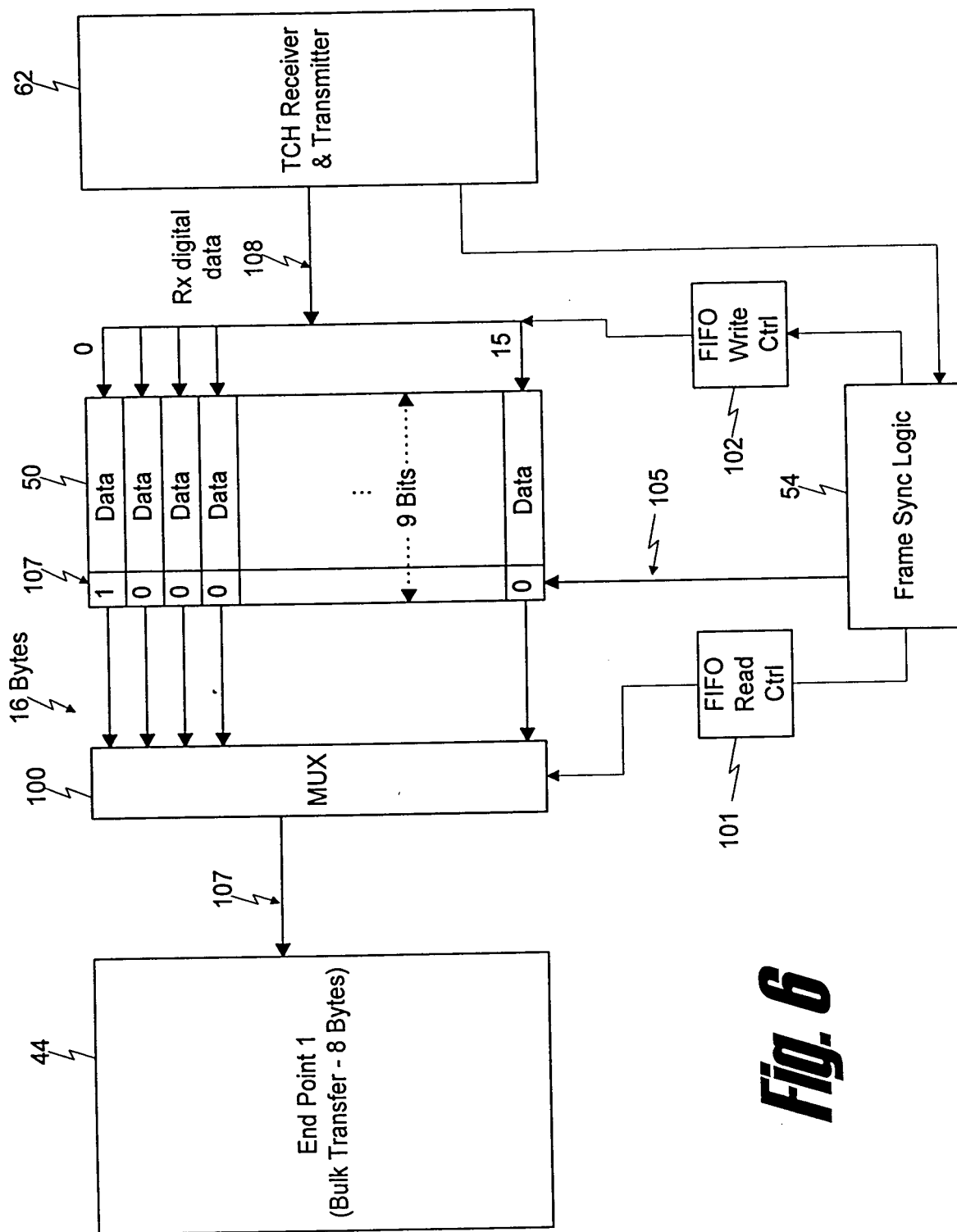
**Fig. 3**



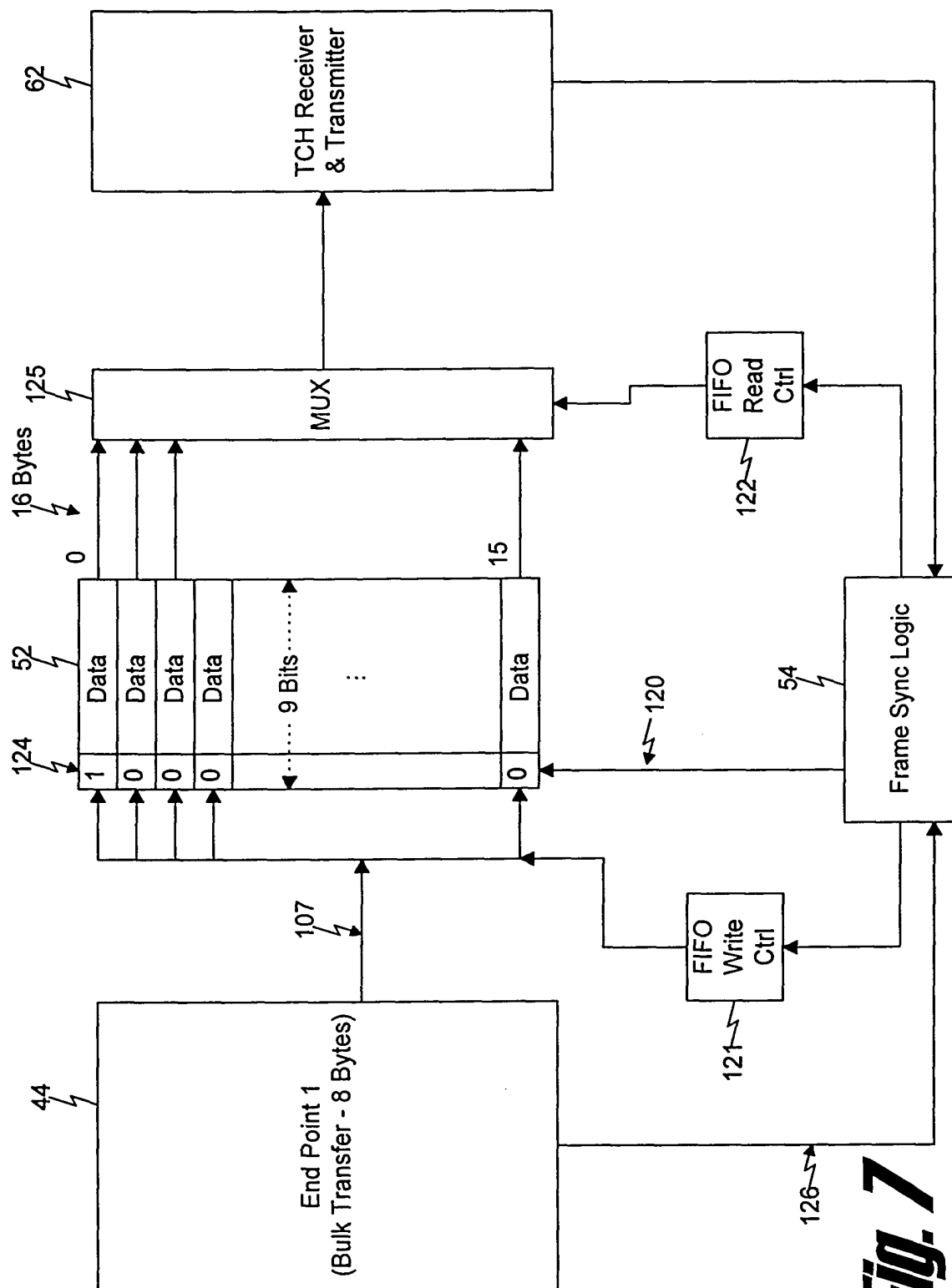
**Fig. 4**



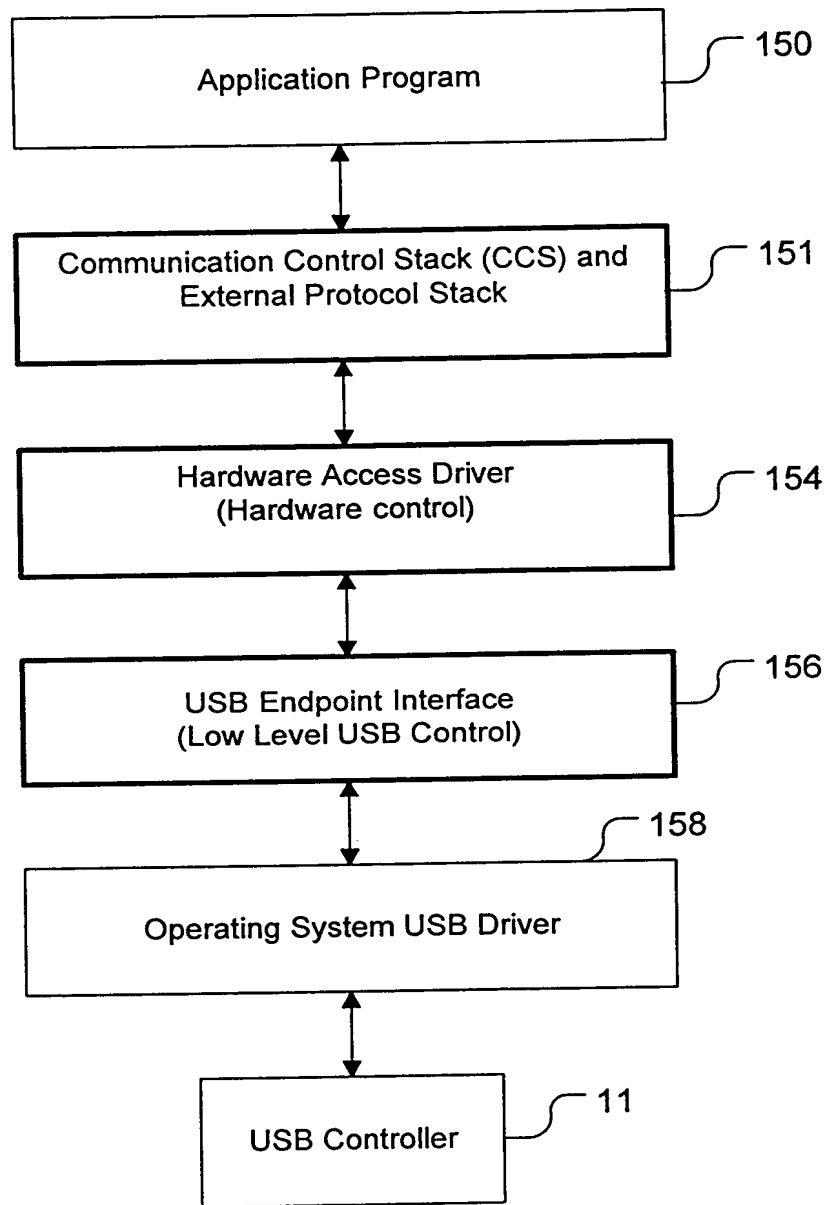
**Fig. 5**



**Fig. 6**

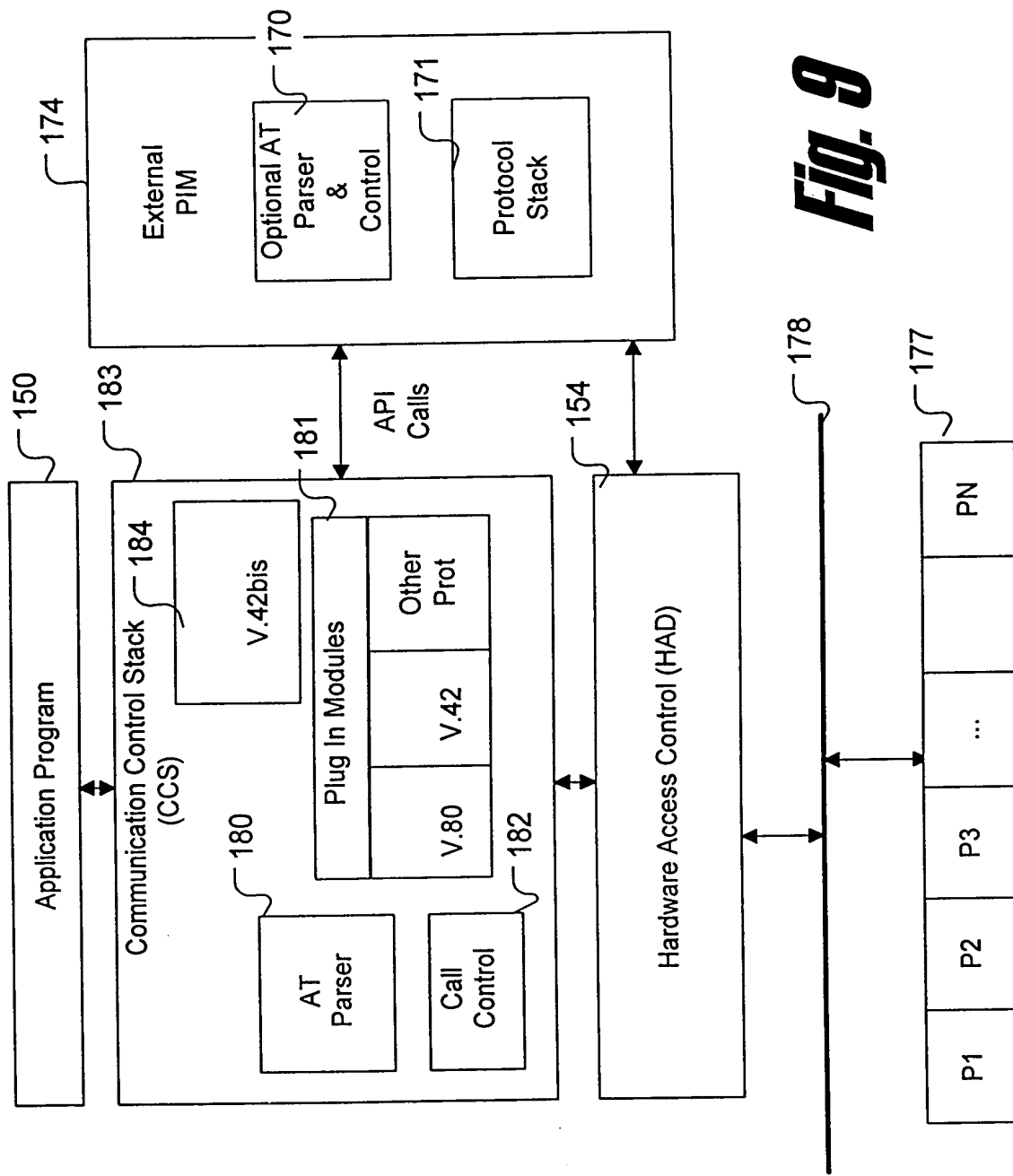


**Fig. 7**

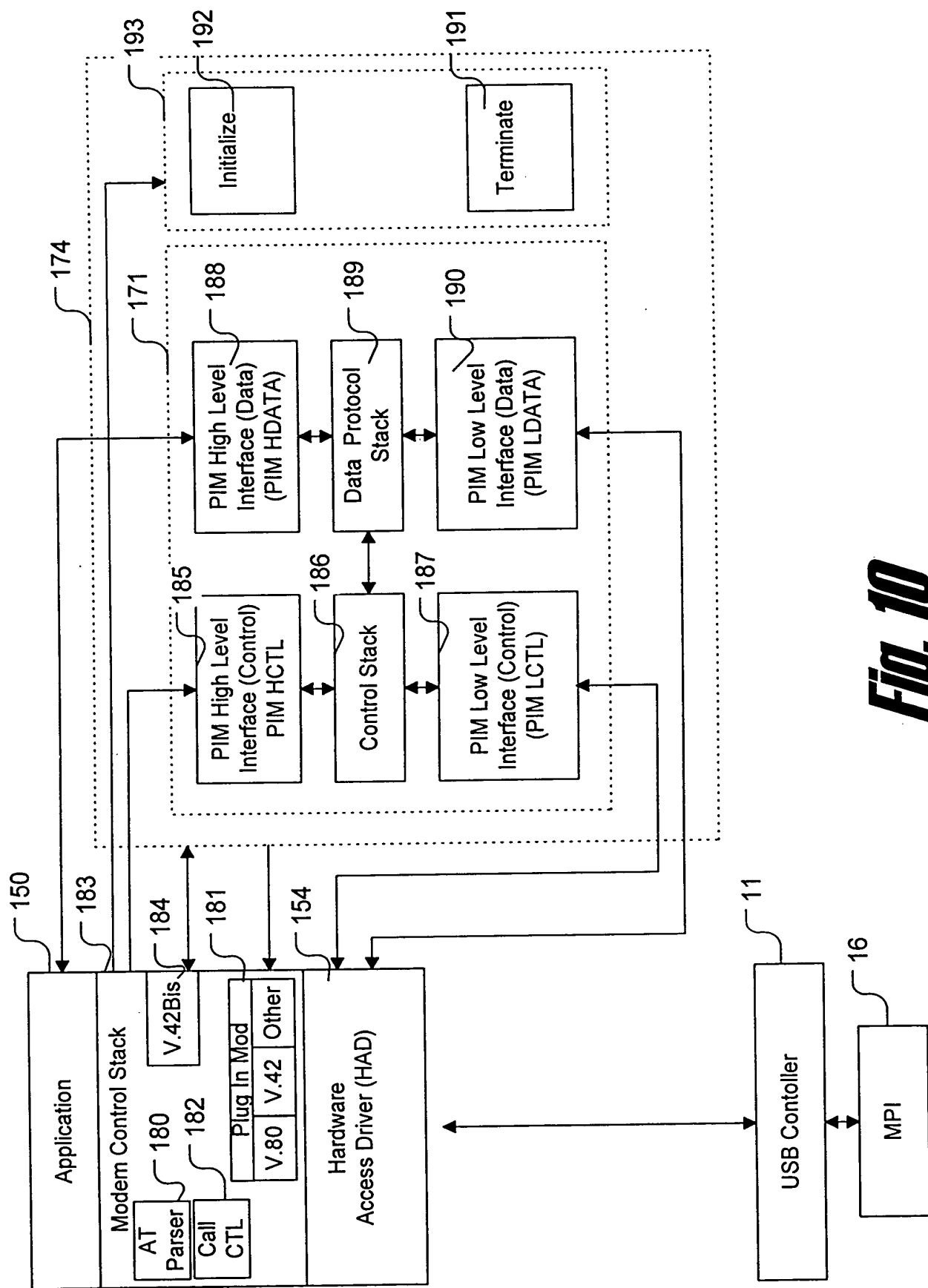


***Fig. 8***





**Fig. 9**



**Fig. 10**

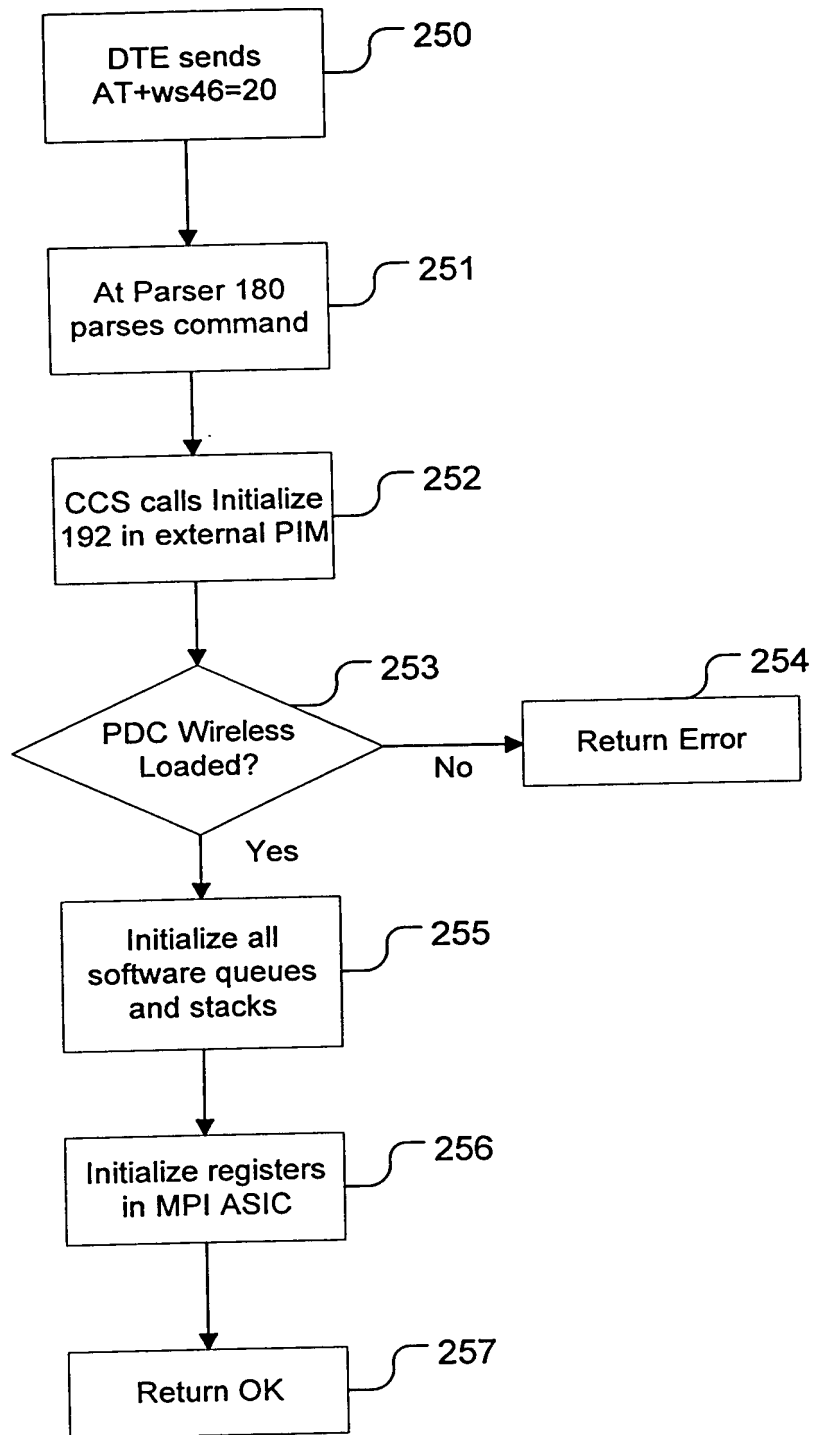
The diagram illustrates the TCH DATA FLOW, showing the path from TCH RX DATA to TCH TX DATA. The flow is organized into several functional blocks and layers:

- TCH RX DATA (225):** The input data at the bottom left.
- DPAL (224):** The Digital Packet Assembly Layer, containing:
  - RX ISR Buffer (225):** Receives TCH RX DATA.
  - HALRxBuffer (224):** Receives data from the RX ISR Buffer.
- PIM\_LDATA (221):** The PIM Layer, containing:
  - qRxEPQ (221):** Receives data from HALRxBuffer.
  - RxFrameBuffer (220):** Receives data from qRxEPQ.
  - ARQ ERROR CONTROL PROTOCOL (218):** Receives data from RxFrameBuffer.
  - qRXqueue (214):** Receives data from the ARQ ERROR CONTROL PROTOCOL.
- PDC (205):** The Packet Data Convergence Layer, containing:
  - cDeCompressBuffer (207):** Receives data from qRXqueue.
  - DECODE (209):** Receives data from cDeCompressBuffer.
  - qDTERxEPQ (205):** Receives data from DECODE.
- DTE (201):** The Data Transfer Entity, containing:
  - qRxQ (202):** Receives data from qDTERxEPQ.
  - RX BUFFER (201):** Receives data from qRxQ.
- TCH TX DATA (224):** The output data at the bottom right.
- DPAL (223):** The Digital Packet Assembly Layer, containing:
  - TX ISR Buffer (224):** Receives data from the TX PATH.
  - HALTXBuffer (223):** Receives data from the TX PATH.
- PIM\_LEX (222):** The PIM Layer, containing:
  - qTxEPQ (222):** Receives data from the TX PATH.
  - TxFrameBuffer (215):** Receives data from qTxEPQ.
  - ARQ ERROR CONTROL PROTOCOL (218):** Receives data from TxFrameBuffer.
  - qTXqueue (211):** Receives data from the ARQ ERROR CONTROL PROTOCOL.
- PDC (204):** The Packet Data Convergence Layer, containing:
  - cInCompressBuffer (206):** Receives data from qTXqueue.
  - ENCODE (208):** Receives data from cInCompressBuffer.
  - qDTEtxEPQ (204):** Receives data from ENCODE.
- DTE (200):** The Data Transfer Entity, containing:
  - qTxQ (203):** Receives data from qDTEtxEPQ.
  - TX BUFFER (200):** Receives data from qTxQ.
- TCH TX DATA (224):** The output data at the bottom right.

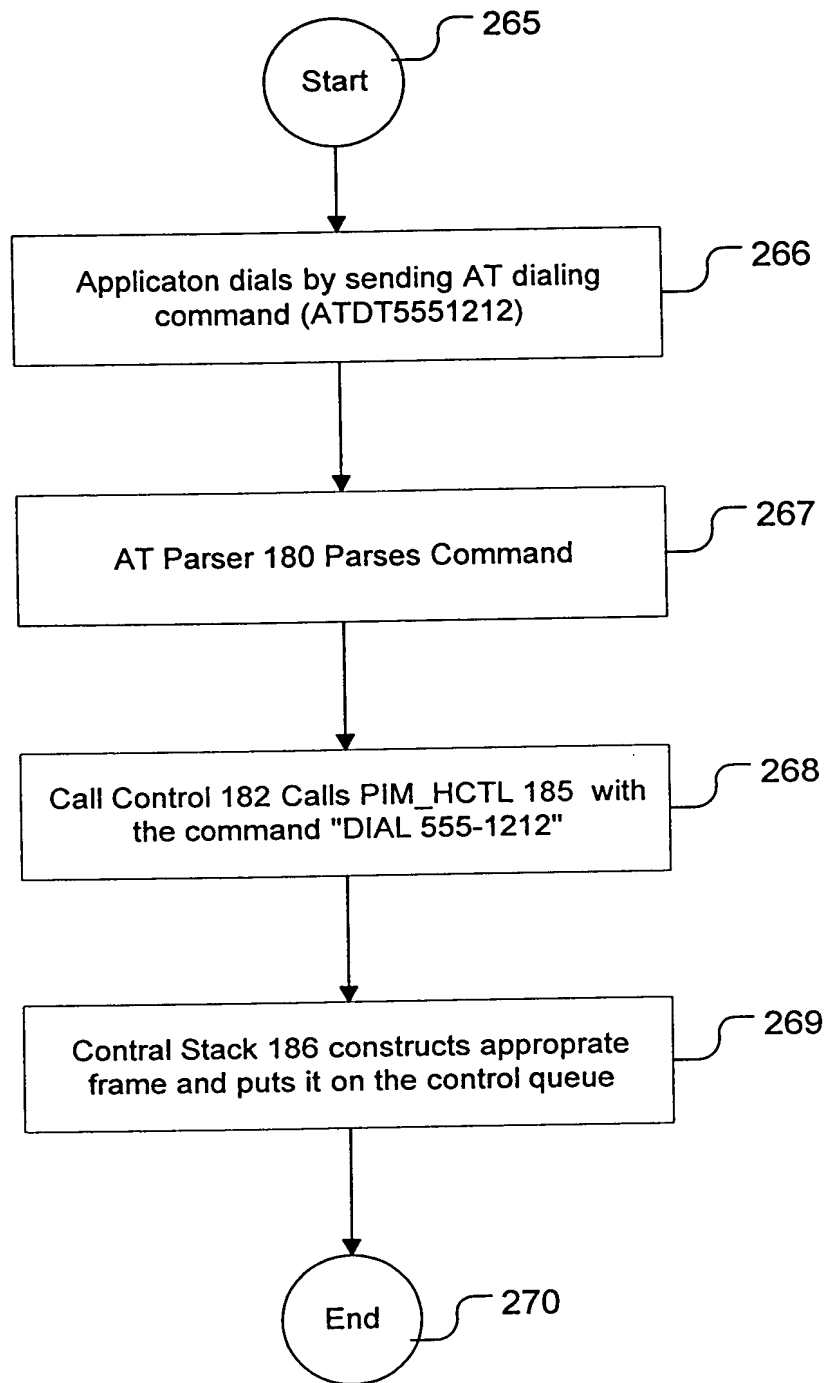
Additional labels and connections include:

- PIM\_HEX (203):** A label for the PDC layer.
- PIM\_HDATA (204):** A label for the PDC layer.
- WINAC\_PIM (222):** A label for the PIM layer.
- WIRE\_PIM (DPAL\_PIM) (223):** A label for the DPAL layer.
- 185:** A label for the PDC layer.
- 184:** A label for the PDC layer.
- 189:** A label for the PDC layer.

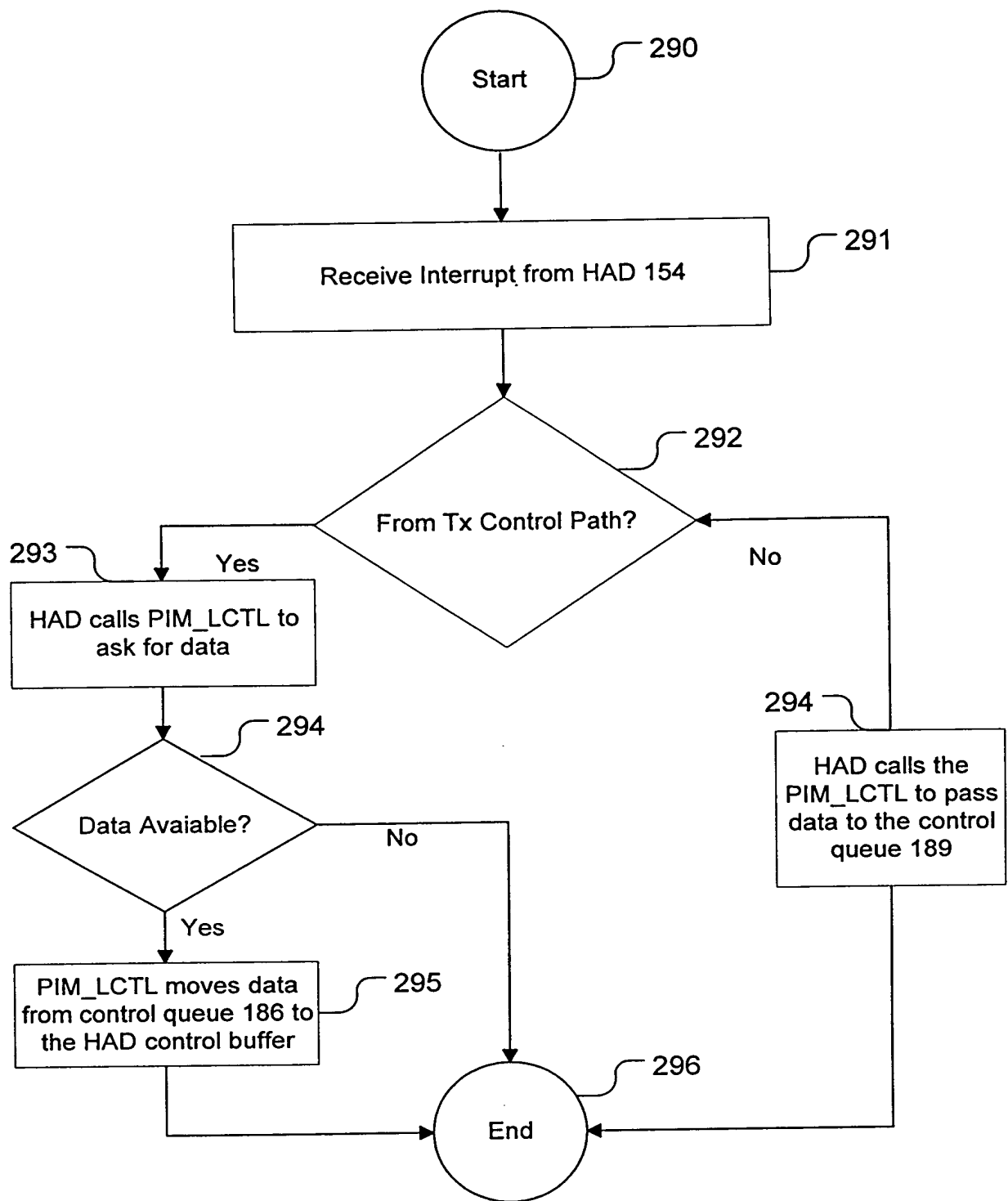
**Fig. 11**



***Fig. 12***

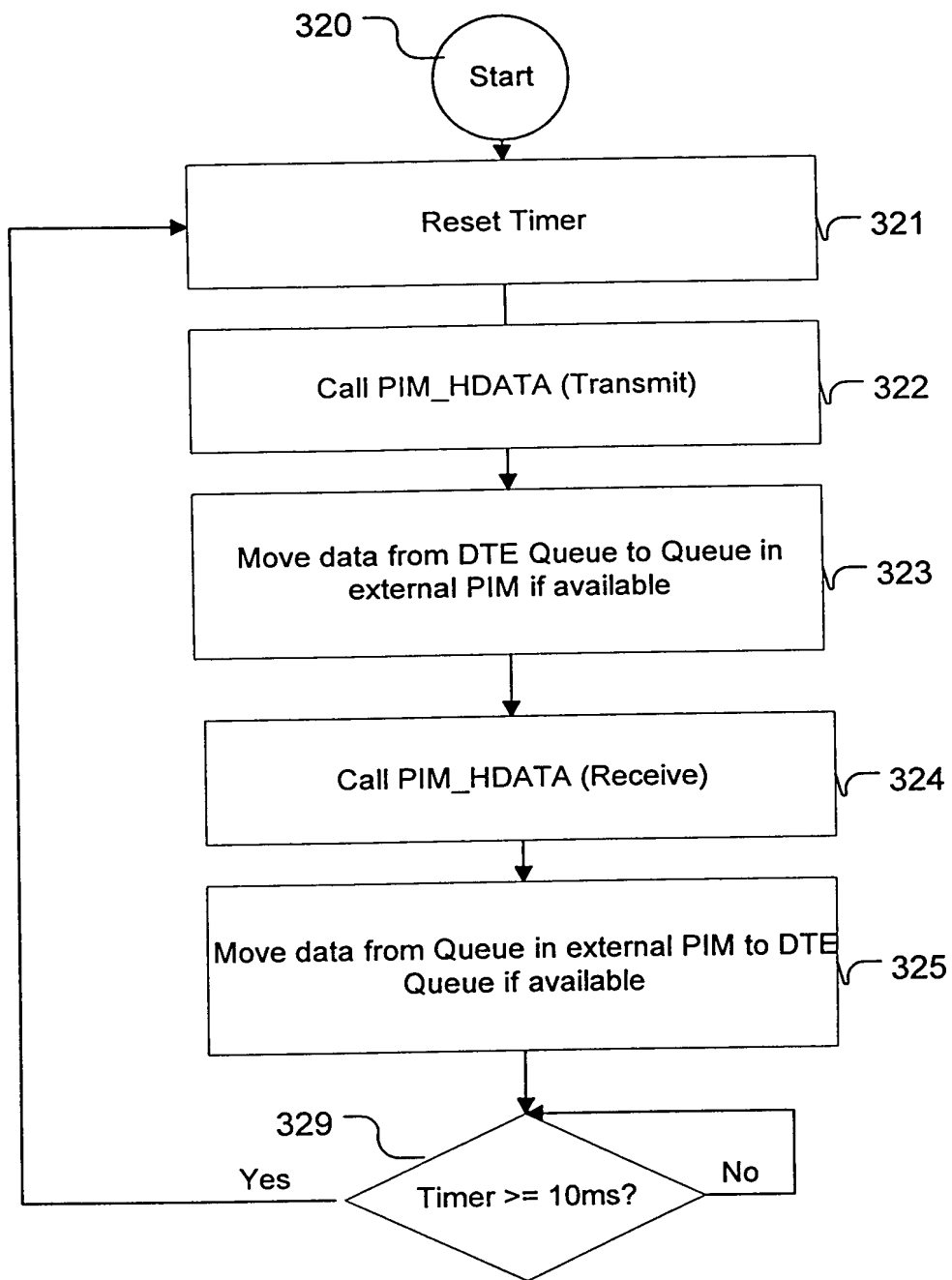


***Fig. 13A*** Control Flow High Level



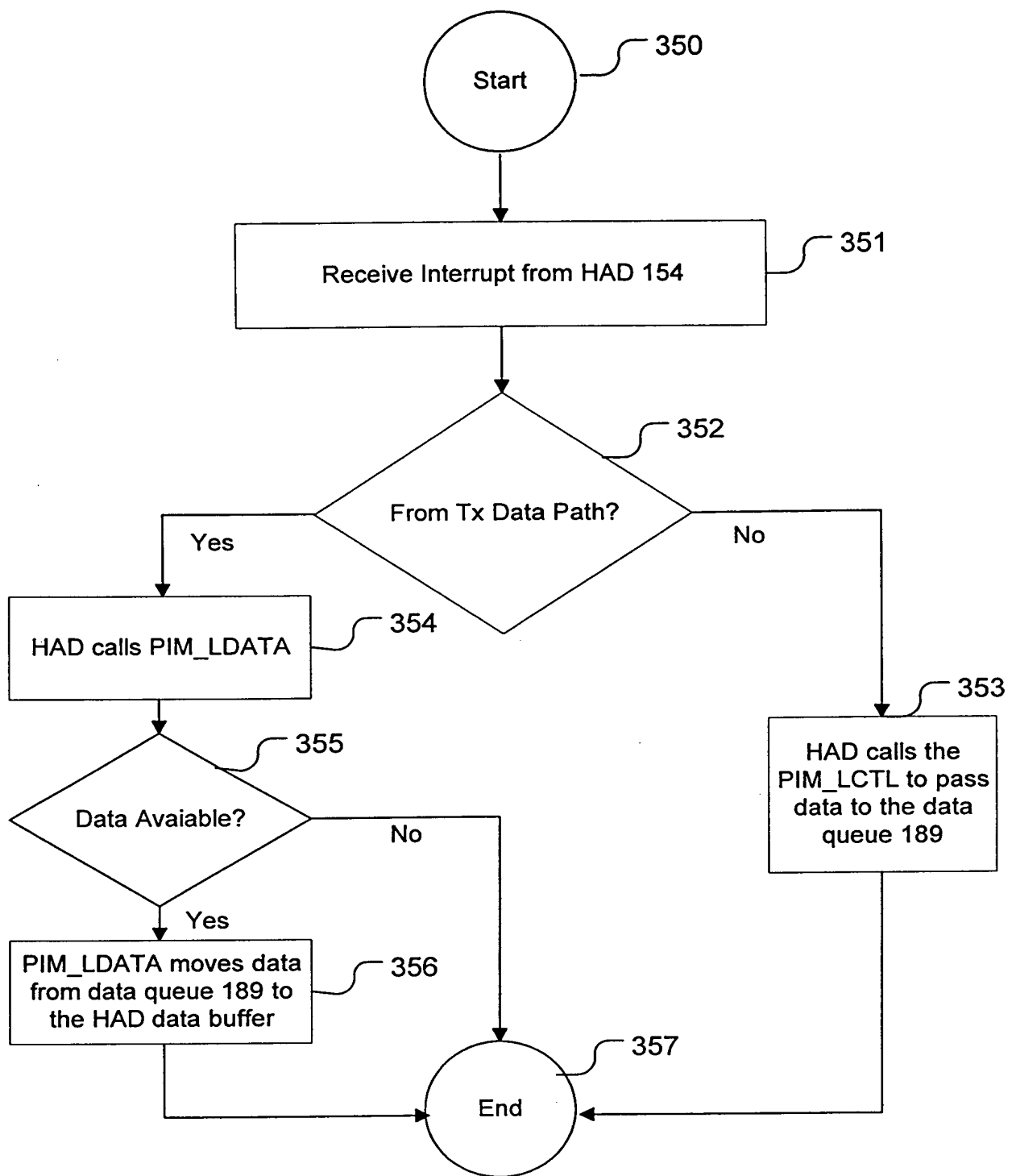
***Fig. 13B***

Control Flow Low  
Level



**FIG. 14A**

Data Flow High  
Level



***Fig. 14B***

Data Flow Low  
Level